Please type a plus sign (+) inside this box → [+]

OIPE JC100 OCT 0 1 2003

PTO/SB/08A (08-00)
Approved for use through 10/31/2002. OMB 0651-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known	
Application Number	10/600,999
Filing Date	June 20, 2003
First Named Inventor	William H. Robertson et al.
Group Art Unit	2825
Examiner Name	J. WHITMORE
Attorney Docket Number	CA7010223001

Sheet	1	of	2

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.[1]	U.S. Patent Document Number	Kind Code[2] (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/W/	1	5,862,223	A	Walker et al.	01-1999	
	2	5,878,408	A	Van Huben et al.	03-1999	
	3	5,933,356	A	Rostoker et al.	08-1999	
	4	6,058,426	A	Godwin et al.	05-2000	
	5	6,102,961	A	Lee et al.	08-2000	
	6	6,141,724	A	Butler et al.	10-2000	
	7	6,269,467	B1	Chang et al.	07-2001	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Office[3]	Number[4]	Kind Code[5] (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
/W/	8	EP	0433066		Hewlett Packard	06-1991		
/W/	9	EP	0944002		Sony Europe	09-1999		
/W/	10	EP	1063599		Fujitsu	12-2000		
/W/	11	PCT	WO 0165423		Cadence	09-2001		

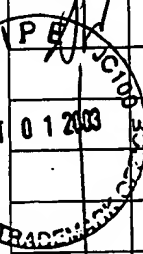| Examiner Signature		Date Considered	1/5/06

PA:52118596.1/2019535-7010223001

PTO/SB/08A (08-00)
Approved for use through 10/31/2002. OMB 0651-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	2	of	2

Complete if Known	
Application Number	10/600,999
Filing Date	June 20, 2003
First Named Inventor	William H. Robertson et al.
Group Art Unit	2825
Examiner Name	S. WHITMORE
Attorney Docket Number	CA7010223001

Examiner Initials	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	12	Benini, L. et al., "Distributed EDA tool integration: the PPP paradigm," Proceedings of the International Conference on Computer Design, 10/1996, IEEE, pgs. 448-453	
	13	Blanchard, D., "EDA on the WWW PCB CAD," Printed Circuit Design, Miller Freeman, 02/1998, vol. 15, no. 2, pgs. 34-37	
	14	Brglz, F. et al., "A universal client for distributed networked design and computing", Jun. 2001, IEEE, 401-406	
	15	Chan et al., "WELD – An Environment for Web-Based Electronic Design," Proceedings of the 35th Design Automation Conference, 05/1998, pgs. 146-151	
	16	Dalpasso, M. et al., "Specification and Validation of Distributed IP-based designs with JavaCAD," 03/1999, IEEE, pgs. 684-688	
	17	Dalpasso, M. et al., "Virtual simulation of distributed IP-based designs", Jun. 1999, IEEE, pp. 50-55	
	18	Dieckman, D. et al., "DISCOE: distributed design and analysis to preserve intellectual property", Sep. 1998, IEEE, pp 57-60	
	19	Geppert, L., "The Rising Tide of Web-Based Tools and Services Augurs the Next Sea Change in Electronic Design IC Design on the World Wide Web," IEEE Spectrum, 06/1998, vol. 35, no. 6, pgs. 45-50	
	20	Konduri et al., "A Framework for Collaborative and Distributed Web-Based Design," Proceedings of the 36th Design Automation Conference, 06/1999, pgs. 898-903	
	21	Larson, N. et al., "Managing Design Processes: A Risk Management Approach," 11/1996, IEEE, pgs. 749-759	
	22	Lee, Dong-Eun, et al., "CADIC: computer-aided design on internet with cryptosystem", Oct. 1998, IEEE, pp. 2670-2674	
	23	Moretti, "Got IP," Printed Circuit Design, Miller Freeman, 10/1998, vol. 15, no. 10, pgs. 31-33	
	24	Orfali, Robert et al., "The essential client/server survival guide", 1996, Katherine Showalter, 2nd ed., pp 228-233 & 476	
	25	Perrin, B., "Web-Based Circuit Engineering," Circuit Cellar Ink, 03/1999, no. 104, pgs. 28-30, 33, 35, 37-38	
	26	Rao et al., "Web-Based Network Analysis and Design," ACM Transactions on Modeling and Computer Simulation, 01/2000, vol. 10, issue 1, pgs. 18-38	
	27	Schindler, P. et al., "IP repository, a Web based IP reuse infrastructure," Custom Integrated Circuits, 05/1999, IEEE, pgs. 415-418	
	28	Wen et al., "Concurrent-Simulation-Based Remote IP Evaluation over the Internet for System-On-Chip Design," 14th International Symposium on System Synthesis, 09/2001, pgs. 233-238	
	29	Wilsey, P., "Web-Based Analysis and Distributed IP," Proceedings of the 1999 Winter Simulation Conference, pgs. 1445-1453	
	30	Zorian, Y., "Test requirements for embedded core-based systems and IEEE P1500," 11/1997, IEEE, pgs. 191-197	

Examiner Signature		Date Considered	1/5/06

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] Unique citation designation number. [2] Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.